

Ahmed Mansour Ahmed

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Personal Information

Date of Birth : 1 January 1989.

Nationality : Egyptian.

Military Service: Completed.

Education

- Master of Science (MSc) student at Alexandria University, Communication and Electronic Division, Electrical Department.Faculty of Engineering, Alexandria University, with GPA 3.6.
- Bachelor of Engineering (BSc) from Electrical Department on Communication and Electronic - Faculty of Engineering, Alexandria University (2011)
- Total Academic Grade is Very Good with honor 83.7 %.
- Excellent Grade at Graduation Project.
- El-Abasiya Military High School, Alexandria with Grade 97.6%.(2006).

Experience

- Digital System Design Engineer and Researcher at Varkon Semiconductor (from 2012).
- Teaching Assistant Managing Director at Alexandria University (2012-2014).
- Teaching Assistant at Air Defence Collage (2011-2012).
- Working in " Downlink Physical Layer Implementation on DSP for LTE-Advanced" Graduation Project 2011 "Excellent Degree".
- IEEE Volunteer (Registration and Activities team) at Alexandria Student Branch (IEEE AlexSB).(2009 - 2011)

Training

- Air Defence Collage LABs.(2011-2012)
- Faculty Of Engineering , Alexandria University & OGS Company ,Alexandria on PLC SIMATIC-S7 Basic. (2011)
- Faculty of Engineering ,Alexandria University , IEEE Alex SB on VHDL Course .(2010)
- Faculty of Engineering, Alexandria University, IEEE Alex SB .on MATLAB level II Course & Simulink.(2010)
- Jelecome Training center on PIC microcontroller . (2010)
- Egypt Telecom (2010).
- Ameriya Oil Refining on DCS FOXBORO System .(2009)
- Alex Training Center on AVR microcontroller .(2009)
- Faculty of Engineering ,Alexandria University , IEEE Alex SB on MATLAB level I Course . (2009)
- New Horizons on Cisco Certified Network Associate "CCNA" . (2009)
- Faculty of Engineering ,Alexandria University Electronic Labs.(2008)

Computer Skills

- MATLAB Programming.
- Latex Writting.
- VHDL software & VLSI software Design and Simulation .
- Multi Sim electronic circuit simulation program.
- Adobe Flash MX & CS2 & Photo shop CS5 & Video Maker.
- C language .
- HTML.
- Eagle PCB design.
- Microsoft Office ,Power Point, Excel.

Language

- Mother Tongue : Arabic (Fluent).
- Second Language : English (Good in speaking and writing).
- Third Language : French (Basic).

Graduation Project & Related Work

Graduation project was "Downlink Physical Layer Implementation on DSP for LTE-Advanced" and I worked on MIMO Equalizer Techniques such as (Sphere Decoder , Kbest, Zero Forcing and Zero Forcing Successive Interference Cancellation), also write c-code of most of blocks to implement it on DSP Kit.

During working at Varkonsemiconductor I worked on LTE 3GPP Project on Link Adaptation such as (channel quality indicator , precoding matrix indicator and rank indicator) and implement some prototypes such as (Log2 module , square root module , natural logarithm module) and deal with cordic algorithms and implement it , also I implement some block in fixed point model such as (Noise variance estimation , CQI SISO , SIMO and MIMO versions , log2 using cordic algorithm).

During working at collage as Teaching Assistant I teach some courses are:

- DSP Implementation of Communication System.
- Digital Integrated Circuit Design.
- Microprocessor System.
- VLSI Fabrication and design.
- Embedded Systems.
- MathI , MathII.

Premaster Courses

- Numerical Analysis of Microwave antenna.
- Theory of Cryptography.
- Mathematical modelling.
- Modelling of stochastic systems.
- Implementation of Digital Signal Processing Circuits.
- Adaptive Signal Processing.
- Acoustic Sensors.
- Biomedical Imaging Processing.

Skills

- Working under pressure.
- Effective in work with team or individual
- Good communication skills.
- Ability to learn creativity in work.

Publications

Conference

- Ahmed M.Mansour, Ali M. El-Sawy, Ahmed.T.Sayed, "A New Floating Point Implementation of Base2 Logarithm," International Conference on Digital Signal Processing,Barcelona, Spain, Oct 2014 - Accepted.

Journal

- Ahmed M.Mansour , Ali M.El-Sawy, Moataz S. Aziz, Ahmed T.Sayed, "A New Efficient Hardware Implementation of Base2 Logarithm for FPGA,"International Journal of Signal Processing Systems (IJSPPS), Milan, Italy,Nov 2014 -Accepted.